

FIG. 1 is a block diagram of a system 10 for transmitting and receiving data. The system 10 includes a Transmitter 10 and a Receiver 20. The Transmitter 10 includes a Stream Partitioning System 15, a Stream Prioritization System 14, a Scheme Selection System 16, and a Control System 18. The Receiver 20 includes a Scheme detection system 22. The system 10 is configured to transmit data from the Transmitter 10 to the Receiver 20.

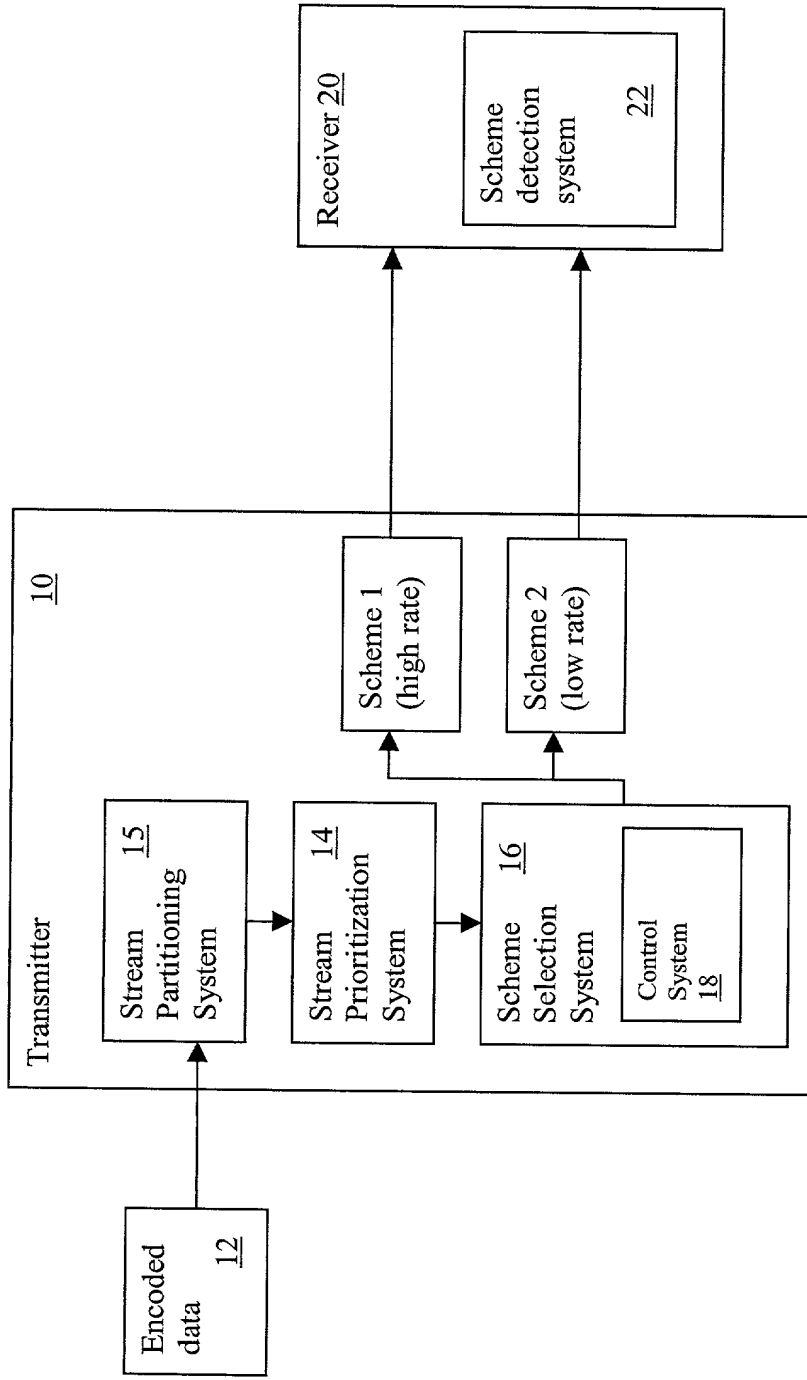


FIG. 1

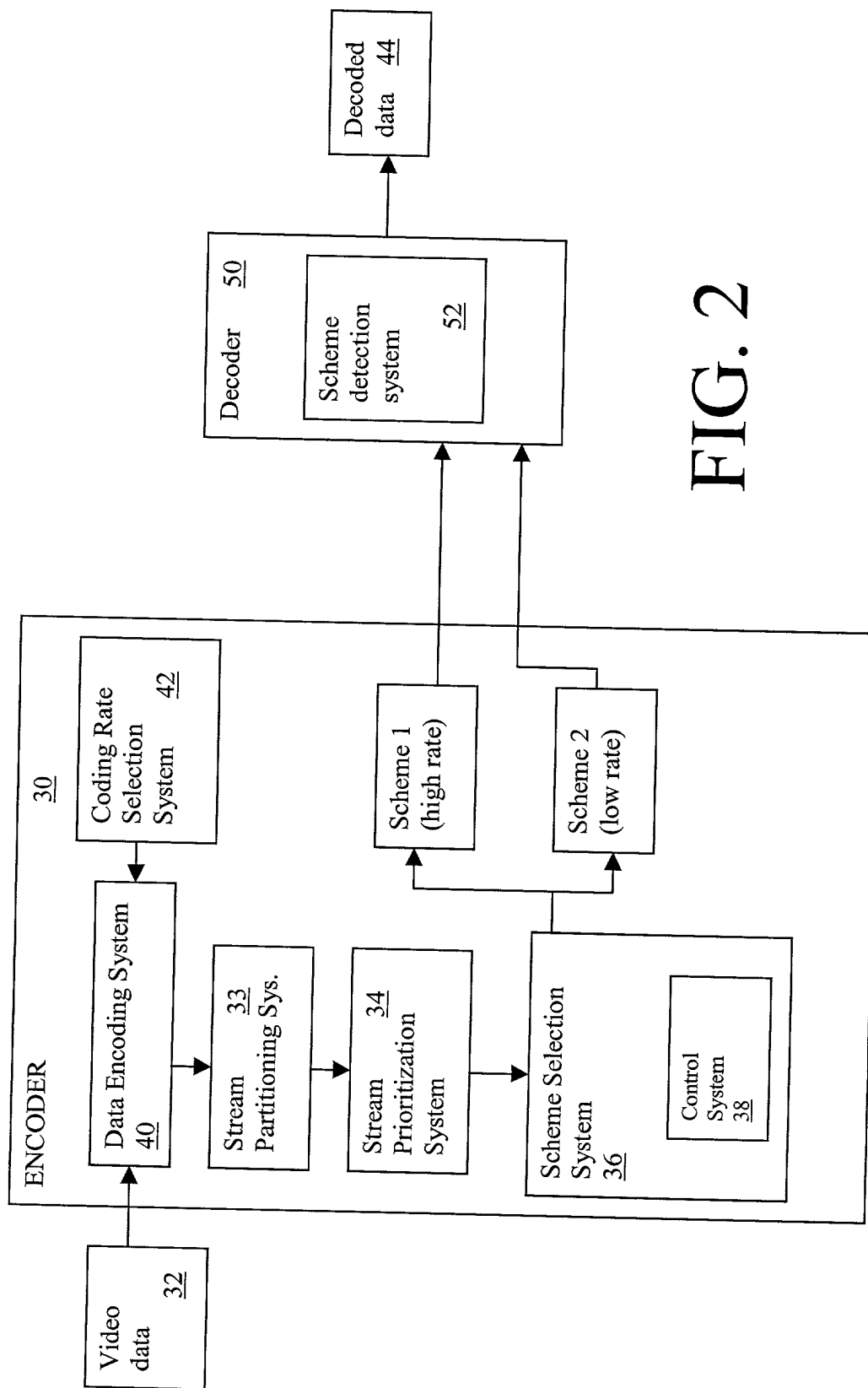


FIG. 2